

AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph beginning on page 2, line 1, with the following amended paragraph:

Moreover, the microcomputer includes a plurality of modes such as a single-chip mode that loads a program contained in the internal ROMs such as the program ~~[[EEROM]]~~ EEPROM 4 and mask ROM 12 to be operated, and a microprocessor mode that loads a program from the external memory to be operated in which the port terminal 26 serves as an address bus or a data bus that connects with the external memory. On releasing of each reset, the potential of the CNVSS terminal 21 is latched to be branched to each mode.